IN THE SPECIFICATION

Please replace the paragraph beginning at page 2, line 24, with the following amended paragraph:

The processing system may also comprises comprise first reassembly circuitry, coupled to the first memory circuitry, for reassembling subsets of received packets prior to storing the packets in the first memory circuitry, and at least second reassembly circuitry, coupled to the second memory circuitry, for reassembling at least a portion of the same subsets of packets reassembled by the first reassembly circuitry, prior to storing the packets in the second memory circuitry. The packet subsets are preferably cells.

Please replace the paragraph beginning at page 6, line 20, with the following amended paragraph:

The packet processing system 300 includes a network processor 302 with a memory 304 and a traffic manager 306 with a memory 308. It is to be understood that in this exemplary embodiment, the network processor 302 with memory 304 represents one processor or processing circuitry with its corresponding reassembly memory circuitry, while the traffic manager 306 and memory 308 represent the other processor or processing <u>circuitry</u> with its corresponding reassembly memory circuitry.